



Calibre was established in 1988 and is respected World-wide for providing image scaling technology for use with large LED video walls primarily used for Concerts, Award Ceremonies, Rental/Staging and Advertising and for image scalers for professional projection applications. Calibre is a highly effective OEM partner to many well-known companies in specialist markets, providing customised image scaling and processing solutions.

As a market leader in innovation Calibre will continue to utilise its own in-house hardware and software design teams for all product development with technology centres in Bradford, UK and Hannover, Germany and its manufacturing facility in Bradford, UK. This enables Calibre to optimise product performance and retain an unusually high depth of technical knowledge of design and innovation.

The Parent Company is Coretronic Corporation with its Head office in Taiwan.

Senior FPGA & Digital Design Engineer

We are looking for a Senior Digital Design Engineer to join a growing team of highly skilled and innovative engineers. We research, design and manufacture high end Pro-AV products and maintain a very high degree of control over both hardware and software, from the low level DSP algorithms through to the User Interface and control systems.

We present an opportunity for you to bring something new to the team whilst offering back the chance to expand your own skillset through close working with engineers who collectively have skills covering the whole software and hardware stack.

Position:

Senior FPGA & Digital Design Engineer: Pro-AV Platform Development Group

The successful candidate must have experience of developing optimised high speed FPGA IP. Is highly driven with a passion for creating great technology. Is from a video or image processing background.

Responsibilities:

- Work within the Core IP and IO-IP Research teams to help create various system IP components enabling the implementation of high quality Video Processing algorithms and high speed video interconnect.
- Work with the Product Development group to implement advanced features using platform IP to add commercial value to various products.
- Assisting R&D Leader with project reporting, tracking and team management.

CALIBRE UK LIMITED

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REGISTERED IN ENGLAND No: 2549739



Required Experience and Qualifications:

- Bachelor's Degree or greater in an Electronics Engineering or related discipline.
- 10+ years of experience writing VHDL.
- Experience of Altera/Intel FPGA development tools to produce designs optimised for high speed operation at a minimal resource cost. eg: TimeQuest, Modelsim, SignalTap.
- A solid understanding of embedded systems.
- Writing or the ability to contribute to the writing of low level device drivers using C/C++ ideally, in an embedded system.
- Knowledge of high speed serial transceiver interfaces.
- Experience using algorithm design tools such as Matlab and Simulink or equivalents.
- Knowledge and understanding of Basic video timing standards in addition to physical interface and connectivity standards.
- Excellent applied mathematical skills.
- Proven experience of communicating at all levels.

Desirable Experience:

- Ability to read schematics.
- Embedded Linux experience (Yocto).

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